IN THE UNITED STATES DISTRICT COURT
FOR THE DISTRICT OF DELAWARE

POWER INTEGRATIONS, INC.,	)	
	)	
Plaintiff,	)	
	)	
v.	)	C.A. No. 07-633-JJF-LPS
	)	
BCD SEMICONDUCTOR CORPORATION and SHANGHAI SIM-BCD SEMICONDUCTOR MANUFACTURING CO., LTD.,	)	
	)	
Defendants.	)	

DEFENDANTS' SUPPLEMENTAL BRIEF IN OPPOSITION TO PLAINTIFF POWER INTEGRATIONS' MOTION FOR ENTRY OF PRELIMINARY INJUNCTION

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Attorneys for Defendants BCD Semiconductor Corporation and Shanghai SIM-BCD Semiconductor Manufacturing, Co., Ltd.

Dated: May 21, 2008

In their Answering Brief in Opposition to Plaintiff Power Integrations' Motion For Entry of Preliminary Injunction ("Answering Brief," D.I. 61), BCD Semiconductor Corporation and Shanghai SIM-BCD Semiconductor Manufacturing, Co., Ltd. (collectively "BCD") included two sections devoted to the reexamination of U.S. Patent No. 6,249,876 (the '876 patent). Answering Brief at 9-12; 23. Although the Patent and Trademark Office ("PTO") had not issued an action on the merits when BCD prepared its Answering Brief, BCD noted the PTO had found that three items of prior art (the Martin, Habetler, and Wang references) "raise a substantial question of patentability." This finding showed that BCD's invalidity defenses, which relied on the same three items of prior art, were substantial enough to defeat PI's motion. *Id.* at 23.

In its Reply Brief in Support of Its Motion for Entry of a Preliminary Injunction ("Reply Brief," D.I. 91), Power Integrations ("PI") called BCD's invalidity defenses a "ruse" (Reply Brief at 12), and asserted "there is no reason to believe" the PTO will find the claims unpatentable (*Id.* at 11 n.8). In making these arguments, however, PI failed to disclose to the Court that the PTO, in an Office Action mailed a month before PI filed its Reply Brief,[1] had found claim 1 of the '876 patent (and every claim under review) invalid over the same prior art BCD identified in its Answering Brief. Ex. A (Apr. 7, 2008 Office Action). The Office Action thus directly contradicts PI's rebuttal to BCD's invalidity argument.

The Office Action's relevance to this case cannot seriously be disputed. *See Gould v. Control Laser Corp.*, 705 F.2d 1340, 1342 (Fed. Cir. 1983) ("One purpose of the reexamination procedure is to eliminate trial of that issue [validity] (when the claim is canceled) or to facilitate trial of that issue by providing the district court with the expert view of the PTO (when a claim

[1] Unbeknownst to BCD, which is not involved in the reexamination proceeding, the Office Action was mailed the same day BCD filed its Answering Brief.

survives the reexamination proceeding).") For instance, even though the rejection is not final, unless PI can convince the PTO to withdraw its rejection, PI will not be able to enforce claim 1 of the '876 patent against BCD. *Id.*; *Broadcast Innovation, L.L.C. v. Charter Communs.*, Inc., 2006 U.S. Dist. LEXIS 46623, *7 (D. Colo. July 11, 2006) ("Importantly, a decision by the Patent Office that the reexamined claims of an issued patent are canceled as unpatentable renders the claims unenforceable in the pending litigation and in any future disputes."). This is true regardless of whether the Court grants Fairchild's post-trial motion concerning the validity of the '876 patent. *Bausch & Lomb, Inc. v. Alcon Lab., Inc.*, 914 F. Supp. 951, 953 (W.D.N.Y. 1996) ("a finding by this Court that the '607 patent is not invalid would not bind the PTO"). PI's singular reliance on the Fairchild jury verdict to support a substantial likelihood of success in this case (*see* Reply Brief at 11-14) pales before the Office Action PI kept from the Court.

The PTO's rejection of all the claims under review confirms that there exists a substantial question concerning the validity of claim 1 of the '876 patent. *See* Answering Brief at 23. PI's failure to disclose, discuss, or even provide the Court a copy of the Office Action underscores PI's inability to demonstrate a likelihood of success on the merits and the extent to which its attempt to obtain a preliminary injunction lacks merit.[2]

For all of the foregoing reasons, as well as those set forth in BCD's Answering Brief, BCD respectfully requests that the Court deny PI's motion for a preliminary injunction.

[2] By addressing only this issue, BCD does not mean to suggest that the Reply contains no other matters requiring response. BCD believes it can address those issues at a hearing.

ASHBY & GEDDES

/s/ Tiffany Geyer Lydon

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Dated: May 21, 2008

EXHIBIT A

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
90/008,326	11/09/2006	6249876	10414-25	7651

7590 04/07/2008

EXAMINER	
ART UNIT	PAPER NUMBER

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DATE MAILED: 04/07/2008

Please find below and/or attached an Office communication concerning this application or proceeding.

UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450
www.uspto.gov

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(THIRD PARTY REQUESTER'S CORRESPONDENCE ADDRESS)

Sanjeet K. Dutta

ORRICK, HERRINGTON & SUTCLIFFE LLP

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EX PARTE REEXAMINATION COMMUNICATION TRANSMITTAL FORM

REEXAMINATION CONTROL NO. *90/008,326*.

PATENT NO. *6,249,876*.

ART UNIT *3992*.

Enclosed is a copy of the latest communication from the United States Patent and Trademark Office in the above identified *ex parte* reexamination proceeding (37 CFR 1.550(f)).

Where this copy is supplied after the reply by requester, 37 CFR 1.535, or the time for filing a reply has passed, no submission on behalf of the *ex parte* reexamination requester will be acknowledged or considered (37 CFR 1.550(g)).

Office Action in Ex Parte Reexamination	Control No. 90/008,326	Patent Under Reexamination 6249876	
	Examiner Christopher E. Lee	Art Unit 3992	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

a☐ Responsive to the communication(s) filed on _____ . b☐ This action is made FINAL.
c☒ A statement under 37 CFR 1.530 has not been received from the patent owner.

A shortened statutory period for response to this action is set to expire 2 month(s) from the mailing date of this letter. Failure to respond within the period for response will result in termination of the proceeding and issuance of an *ex parte* reexamination certificate in accordance with this action. 37 CFR 1.550(d). **EXTENSIONS OF TIME ARE GOVERNED BY 37 CFR 1.550(c).** If the period for response specified above is less than thirty (30) days, a response within the statutory minimum of thirty (30) days will be considered timely.

Part I THE FOLLOWING ATTACHMENT(S) ARE PART OF THIS ACTION:

1. ☐ Notice of References Cited by Examiner, PTO-892.
2. ☒ Information Disclosure Statement, PTO/SB/08.
3. ☐ Interview Summary, PTO-474.
4. ☐ _____.

Part II SUMMARY OF ACTION

1a. ☒ Claims *1 and 17-19* are subject to reexamination.
1b. ☒ Claims *2-16 and 20-32* are not subject to reexamination.
2. ☐ Claims _____ have been canceled in the present reexamination proceeding.
3. ☐ Claims _____ are patentable and/or confirmed.
4. ☒ Claims *1 and 17-19* are rejected.
5. ☐ Claims _____ are objected to.
6. ☐ The drawings, filed on _____ are acceptable.
7. ☐ The proposed drawing correction, filed on _____ has been (7a)☐ approved (7b)☐ disapproved.
8. ☐ Acknowledgment is made of the priority claim under 35 U.S.C. § 119(a)-(d) or (f).
   a)☐ All b)☐ Some* c)☐ None of the certified copies have
   1☐ been received.
   2☐ not been received.
   3☐ been filed in Application No. _____ .
   4☐ been filed in reexamination Control No. _____.
   5☐ been received by the International Bureau in PCT application No. _____.
   * See the attached detailed Office action for a list of the certified copies not received.
9. ☐ Since the proceeding appears to be in condition for issuance of an *ex parte* reexamination certificate except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte* Quayle, 1935 C.D. 11, 453 O.G. 213.
10. ☐ Other: _____

cc: Requester (if third party requester)

Ex Parte Reexamination

1. This is an *Ex Parte* Reexamination of US 6,249,876 A ("the 876 Patent"). Currently, claims 1 and 17-19 are pending in this *Ex Parte* Reexamination. The claims 2-16 and 20-32 are not subject to reexamination.

Information Disclosure Statement

2. The Information Disclosure Statement filed on 26th of October 2007 fails to comply with the provisions of 37 CFR 1.97, 1.98 and MPEP § 609 because (1) the references without providing publisher, and/or the references without providing proper page numbers fail to comply with 37 CFR 1.98(b)(5), which requires a publisher, and properly referred pages, and/or (2) the inclusion of the court proceedings as the references in the Information Disclosure Statement. The court proceedings listed in the Information Disclosure Statement have been given due consideration, however, as these are not printed publications per se. The listings on the Information Disclosure Statement have been lined through and these listings will not appear on the Reexamination Certificate.

The *Ex Parte* Reexamination Requester is advised that the date of any re-submission of any item of information contained in the Information Disclosure Statement or the submission of any missing element(s) will be the date of submission for purposes of determining compliance with the requirements based on the time of filing the statements, including all certification requirements for statements under 37 CFR 1.97(e), respectively. See MPEP § 609.05(a).

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Martin, Jr. et al. [US 4,638,417; hereinafter "Martin"].

Referring to claim 1, Martin discloses a digital frequency jittering circuit (i.e., power density spectrum controller) for varying the switching frequency of a power supply (i.e., a circuit maintaining the average power of a large number of operating frequencies of the circuit within the dynamic range of the switching mode of a power supply; See col. 1, lines 49-53), comprising:

- an oscillator (i.e., VCO 13 of Figure) for generating a signal (i.e., oscillating signal) having a switching frequency (See col. 2, lines 46-49), the oscillator (i.e., said VCO) having a control input (i.e., input line from D/A 12 in Figure) for varying the switching frequency (See col. 2, lines 39-49);
- a digital to analog converter (i.e., D/A 12 of the Figure) coupled to the control input (i.e., said input line from said D/A) for varying the switching frequency (See col. 2, lines 44-49); and
- a counter (i.e., COUNTER 10 of the Figure) coupled to the output of the oscillator (i.e., feedback line from said VCO in Figure) and to the digital to analog converter (i.e., said COUNTER being coupled to said D/A through EPROM 11 in Figure; See col. 2, lines 39-46), the counter (i.e., said COUNTER) causing the digital to analog converter (i.e., said D/A) to adjust the control input (i.e., said input line from said D/A) and to vary the switching frequency (See col. 2, lines 20-49).

5. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Wang et al. ["Programmed Pulsewidth Modulated Waveforms for Electromagnetic Interference Mitigation in DC-DC Converters," IEEE Transactions on Power Electronics, Vol. 8, No.4 (October 1993) by A.C. Wang and S.R. Sanders, pp. 596-605; hereinafter "Wang"].

Referring to claim 1, Wang discloses a digital frequency jittering circuit (i.e., PWM type power circuit with a time-varying switching frequency) for varying the switching frequency of a power supply (See page 596, I. Introduction, lines 12-15), comprising:

- an oscillator (i.e., internal wave form generator, in fact, Oscillator between D/A and Comparator, said Comparator, and Simple Control Logic in Fig. 20) for generating a signal (i.e., Internal Waveforms in Fig. 19) having a switching frequency (i.e., switching frequency V_{TR} in Fig. 20), the oscillator (i.e., said internal wave form generator) having a control input (i.e., Ramp Slope in Fig. 20) for varying the switching frequency (See page 604, col. 1, lines 2-7);
- a digital to analog converter (i.e., Subperiod Width D/A in Fig. 20) coupled to the control input (i.e., said Ramp Slope being coupled to output line from said Subperiod Width D/A in Fig. 20) for varying the switching frequency (in fact, Counter causing said Subperiod Width D/A to adjust said Ramp Slope and to vary said switching frequency V_{TR} in Fig. 20; See pages 603-604); and
- a counter (i.e., Counter in Fig. 20) coupled to the output of the oscillator (i.e., said Counter being coupled to said internal wave form generator, actually, said Simple Control Logic, in Fig. 20) and to the digital to analog converter (i.e., said Subperiod Width D/A through ROM in Fig. 20), the counter (i.e., said Counter) causing the digital to analog converter (i.e., said Subperiod Width D/A) to adjust the control input (i.e., said

Ramp Slope from said Subperiod Width D/A) and to vary the switching frequency (See Fig. 20 and pages 603-604).

6. Claims 1 and 17-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Habetler et al. ["Acoustic Noise Reduction in Sinusoidal PWM Drives Using a Randomly Modulated Carrier," IEEE Transactions on Power Electronics, Vol. 6, No. 3, p.356-363 (published July 1991) by T.G. Habetler and D. M. Divan; hereafter "Habetler"].

Referring to claim 1, Habetler discloses a digital frequency jittering circuit (i.e., random carrier sinusoidal PWM regulator in Fig. 5) for varying the switching frequency of a power supply (i.e., for varying the switching frequency of Inverter; See page 359, IV. Implementation), comprising:

- an oscillator (i.e., Triangle Generator of Fig. 5) for generating a signal (i.e., triangle wave) having a switching frequency (i.e., said Triangle Generator generating said triangle wave with a switching frequency, inherently, in Fig. 5), the oscillator (i.e., said Triangle Generator) having a control input (i.e., input from A/D and Average Slope in Fig. 5) for varying the switching frequency (See page 359, IV. Implementation, lines 19+);
- a digital to analog converter (i.e., A/D, in fact, digital to analog converter, in Fig. 5; See page 359, IV. Implementation, lines 28-30) coupled to the control input (i.e., said input from A/D and Average Slope being coupled to output line from said A/D in Fig. 5) for varying the switching frequency (See page 359, IV. Implementation, lines 19+); and
- a counter (i.e., Counter in Fig. 5) coupled to the output of the oscillator (i.e., said Counter being coupled to said Triangle Generator through Peak Detector in Fig. 5) and to the digital to analog converter (i.e., said A/D through EPROM in Fig. 5), the counter (i.e., said Counter) causing the digital to analog converter (i.e., said A/D) to adjust the control

input (i.e., said input from A/D and Average Slope) and to vary the switching frequency (See Fig. 5 and page 359, IV. Implementation, lines 19+).

Referring to claim 17, Habetler discloses a method for generating a switching frequency (i.e., generating triangle wave having a switching frequency in Fig. 5) in a power conversion system (i.e., random carrier sinusoidal PWM regulator in Fig. 5), comprising:

- generating a primary voltage (i.e., Average Slope, as a primary voltage, is provided to addition circuit in Fig. 5);
- cycling one or more secondary voltage sources to generate a secondary voltage (i.e., feedback signal from Triangle Generator being cycled through Counter, EPROM, and A/D as an output voltage, i.e.; a secondary voltage, in Fig. 5) which varies over time (i.e., this circulated signal is varying over time; See Fig. 5); and
- combining the secondary voltage (i.e., said output voltage from A/D in Fog. 5) with the primary voltage (i.e., said Average Slope) to be received at a control input of a voltage-controlled oscillator (i.e., input of Triangle Generator in Fig. 5) for generating a switching frequency which is varied over time (See page 359, IV. Implementation, lines 19+).

Referring to claim 18, Habetler teaches

- clocking a counter (i.e., Counter of Fig. 5) with the output of the oscillator (i.e., said Counter being clocked by triangle wave generated by Triangle Generator in Fig. 5).

Referring to claim 19, Habetler teaches that

- the primary voltage (i.e., Average Slope, as a primary voltage, is provided to addition circuit in Fig. 5) is V and each of the secondary voltage sources generates a

supplemental voltage lower than V (See page 359, V. Experimental Results, lines 3-7, wherein in fact that there is very little perceivable difference in the triangle wave with and without random modulation, since the instantaneous frequency is varying only slightly, which anticipates that output voltage from A/D is lower that said Average Slope voltage because the perceivable difference in the triangle waves between the conventional modulation and the random modulation is very little. In other words, the minor output voltage from said A/D is added to the major voltage of said Average Slope in Fig. 5), and

- passing the supplemental voltage to the voltage-controlled oscillator (i.e., said output voltage from A/D being passed to Triangle Generator for random carrier PWM regulation in Fig. 5; See page 359, IV. Implementation and V. Experimental Results).

NOTICE RE PATENT OWNER'S CORRESPONDENCE ADDRESS

Effective May 16, 2007, 37 CFR 1.33(c) has been revised to provide that:

The Patent Owner's correspondence address for all communications in an *ex parte* reexamination or an *inter partes* reexamination is designated as the correspondence address of the patent.

Revisions and Technical Corrections Affecting Requirements for *Ex Parte* and *Inter Partes* Reexamination, 72 FR 18892 (April 16, 2007)(Final Rule)

The correspondence address for any pending reexamination proceeding not having the same correspondence address as that of the patent is, by way of this revision to 37 CFR 1.33(c), automatically changed to that of the patent file as of the effective date.

This change is effective for any reexamination proceeding which is pending before the Office as of May 16, 2007, including the present reexamination proceeding, and to any reexamination proceeding which is filed after that date.

Parties are to take this change into account when filing papers, and direct communications accordingly.

In the event the Patent Owner's correspondence address listed in the papers (record) for the present proceeding is different from the correspondence address of the patent, it is strongly encouraged that the patent owner affirmatively file a Notification of Change of Correspondence Address in the reexamination proceeding and/or the patent (depending on which address Patent

Owner desires), to conform the address of the proceeding with that of the patent and to clarify the record as to which address should be used for correspondence.

Telephone Numbers for reexamination inquiries:

Reexamination and Amendment Practice	(571) 272-7703
Central Reexam Unit (CRU)	(571) 272-7705
Reexamination Facsimile Transmission No.	(571) 273-9900

Conclusion

The Patent Owner is reminded that any proposed amendment to the specification and/or claims in this reexamination proceeding must comply with 37 CFR 1.530(d)-(j).

In order to ensure full consideration of any amendments, affidavits or declarations, or other documents as evidence of patentability, such documents must be submitted in response to this Office action. Submissions after the next Office action, which is intended to be a final action, will be governed by the requirements of 37 CFR 1.116, after final rejection and 37 CFR 41.33 after appeal, which will be strictly enforced.

After filing of a request for *ex parte* reexamination by a Third Party requester, any document filed by either the Patent Owner or the Third Party requester must be served on the other party (or parties where two or more Third Party requester proceedings are merged) in the reexamination proceeding in the manner provided in 37 CFR 1.248. The document must reflect service or the document may be refused consideration by the Office. See 37 CFR 1.550(f).

Extensions of time under 37 CFR 1.136(a) will not be permitted in these proceedings because the provisions of 37 CFR 1.136 apply only to "an applicant" and not to parties in a reexamination proceeding. Additionally, 35 U.S.C. 305 requires that reexamination proceedings "will be conducted with special dispatch" (37 CFR 1.550(a)). Extension of time in *ex parte* reexamination proceedings are provided for in 37 CFR 1.550(c).

The Patent Owner is reminded of the continuing responsibility under 37 CFR 1.565(a), to apprise the Office of any litigation activity, or other prior or concurrent proceeding, involving the instant Patent Under Reexamination or any related patent throughout the course of this reexamination proceeding. The Third Party requester is also reminded of the ability to similarly inform the Office of any such activity or proceeding throughout the course of this reexamination proceeding. See MPEP §§ 2207, 2282 and 2286.

All correspondence relating to this *ex parte* reexamination proceeding should be directed:

By Mail to:	Mail Stop *Ex Parte* Reexam Central Reexamination Unit Commissioner for Patents United States Patent & Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450
By FAX to:	(571) 273-9900 Central Reexamination Unit
By hand:	Customer Service Window Randolph Building 401 Dulany Street Alexandria, VA 22314

Any inquiry concerning this communication or earlier communications from the Reexamination Legal Advisor or Examiner, or as to the status of this proceeding, should be directed to the Central Reexamination Unit at telephone number (571) 272-7705.

Signed:

/Christopher E. Lee/

Primary Patent Examiner (Reexamination)
Central Reexamination Unit / Art Unit 3992

Conferees:

ESK

GJN

EFS-Web Receipt date: 10/26/2007 90008326 - GAU: 3992

66155 U.S. PTO

Substitute for Form 1449/PTO

10/26/0

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known	
Application Number	90/008,326
Filing Date	November 9, 2006
First Named Inventor:	Balu Balakrishnan
Art Unit	3992
Examiner Name	~~Rubin, Margaret~~
Attorney Docket Number	005510.P033R

Sheet	1	of	5

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	US-	3,491,252	1/20/1970	Petrohilos	
	2	US-	3,555,399	1/21/1971	Buchanan et al	
	3	US-	3,840,797	10/28/1975	Aggen et al	
	4	US-	3,916,224	10/28/1975	Daniels et al	
	5	US-	4,072,965	2/7/1978	Kondo	
	6	US-	4,143,282	3/6/1979	Berard, Jr. et al	
	7	US-	4,228,493	10/14/1980	de Sartre et al	
	8	US-	4,236,198	11/25/1980	Ohsawa et al	
	9	US-	4,495,554	1/22/1985	Simi et al	
	10	US-	4,559,590	12/17/1985	Davidson	
	11	US-	4,622,627	11/11/1986	Rodriguez et al	
	12	US-	4,695,936	9/22/1987	Whittle	
	13	US-	4,706,176	11/10/1987	Kettschau	
	14	US-	4,706,177	11/10/1987	Josephson	
	15	US-	4,720,641	1/19/1988	Faini	
	16	US-	4,725,769	2/16/1988	Cini et al	
	17	US-	4,734,839	3/29/1988	Barthold	
	18	US-	4,739,462	4/19/1988	Farnsworth et al	
	19	US-	4,806,844	2/21/1989	Claydon et al	
	20	US-	4,809,148	2/28/1989	Barn	
	21	US-	4,811,184	3/7/1989	Koninsky et al	
	22	US-	4,814,674	3/12/1989	Hrassky	
	23	US-	4,858,094	8/15/1989	Barlage	
	24	US-	4,862,339	8/29/1989	Inou et al	
	25	US-	4,866,590	9/12/1989	Odaka et al	
	26	US-	4,870,555	9/26/1989	White	

Examiner Signature		Date Considered	

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1]Applicant's unique citation designation number (optional). [2]Applicant is to place a check mark here if English Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

Based on Form PTO/SB/08A (08-03) as modified by BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP on 09/10/03

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /CEL/

EFS-Web Receipt date: 10/26/2007 90008326 - GAU: 3992

Substitute for Form 1449/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(use as many sheets as necessary)*				Application Number	90/008,326
				Filing Date	November 9, 2006
				First Named Inventor:	Balu Balakrishnan
				Art Unit	3992
				Examiner Name	~~Rubin, Margaret~~
Sheet	1	**of**	5	Attorney Docket Number	005510.P033R

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (If known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	27	US-	4,887,199	12/12/1989	Whittle	
	28	US-	4,888,497	12/19/1989	Dallabora et al	
	29	US-	4,890,210	12/26/1989	Meyers	
	30	US-	4,928,220	5/22/1990	White	
	31	US-	4,937,728	6/26/1990	Leonardi	
	32	US-	4,943,903	7/24/1990	Cardwell. Jr.	
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	34	US-	5,014,178	5/7/1991	Balakrishnan	
	35	US-	5,034,871	7/23/1991	Okamoto et al	
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	51	US-	5,552,746	9/3/1996	Danstrom	

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EFS-Web Receipt date: 10/26/2007 90008326 - GAU: 3992

Substitute for Form 1449/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(use as many sheets as necessary)*				Application Number	90/008,326
				Filing Date	November 9, 2006
				First Named Inventor:	Balu Balakrishnan
				Art Unit	3992
				Examiner Name	~~Rubin, Margaret~~
Sheet	4	90/008,326	5	Attorney Docket Number	005510.P033R

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	71	H.S. Hoffman, Jr., "Self-Generated Bias Supply", IBM Technical Discloser Bulletin, Vol. 20, No. 5, October 1977, pp. 1814-4.	
	72	H.S. Hoffman, Jr. et al, "Proportional Drive Supply with Diversion Control". IBM Technical Disclosure Bulletin, Vol. 21, No. 12, May 1979, pp. 4904-5	
	73	A. Halperin, "Primary Regulated Dual Power Supply", IBM Technical Disclosure Bulletin, Vol. 21, No. 10, March 1979m pp.4299-300,	
	74	Riezeman, "5-W dc-dc converters aim at telecomm applications", Electronic Design Vol 31, No. 15, July 21, 1983 pp 227	
	75	"Combined Switch-Mode Power Amplifier and Supply", IBM Technical Disclosure Bulletin, Vol. 28, No. 3, August 1985, pp. 1193-1195.	
	76	R. Bruckner, et al., " Optimizing Converter Design and Performance Utilizing Micro Controller System Feedback Control", Proceedings of Powercon 8, E-2, 1981, pp 1-10	
	77	B. Pelly et al., Power MOSFETs take the load off switching supply design", Electronic Design, February 1983, pp 135-139.	
	78	D. Azzis et al, "Flyback on Card Power Sypply", IBM Technical Disclosure Bulletin, Vol. 23, No. 4, September 1980, pp. 1477-78	
	79	A.J. Bowen et al., " Power Supply with Optical Isolator", IBM Technical Disclosure Bulletin Vol. 14, No. 11, April 1972, pp. 3320	
	80	"Off-Line Power Supply Control Technique Using a Single Transformer to Feed Back Three Control Signals", IBM Technical Disclosure Bulletin, Vol. 32, No. 8A, January 1990, pp.272-3	
	81	PWM Power Supply IC; 85-265 VAC Input Isolated, Regulated DC Output; Power Integrations SMP211 Datasheet (January 1996) ("SMP 211")	

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	76	R. Bruckner, et al., " Optimizing Converter Design and Performance Utilizing Micro Controller System Feedback Control", Proceedings of Powercon 8, E-2, 1981, pp 1-10	
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				First Named Inventor:	Balu Balakrishnan
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				Examiner Name	~~Rubin, Margaret~~
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	82	SGS-Thomson Microelectronics TEA 2262; Switched Mode Power Supply Controller; pages 1-9; April 1996	
	83	SGS-Thomson Microelectronics TEA 2260/TEA2261; High Performance Driver Circuits for S.M.P.S.; pages 1-33; June 1994	
	~~84~~	~~LM 3101 Secondary Side PWM Controller; National Semiconductor; Szopesi~~	
	85	Off-Line Power Integrated Circuit For International Rated 60 Watt Power Supplies; Richard Keller, Power Integrations Inc., Page 505-512, IEEE 1992	
	86	Unitrode UCC 3800/1/2/3/4/5 biCMOS Current Mode Control IC's (1994) ("U-133")	

Examiner Signature	/Christopher E. Lee/	Date Considered	03/25/2008

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of	19

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	3	US-	4,638,417	01-20-1997	H. Martin et al.	
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	20	A.C. WANG & S.R. SANDERS, "Random and Programmed Pulse-Width Modulation Techniques for DC-DC Converters," IEEE 1990	
	21	F.J. DE STASI & T. SZEPESI, "A 5A 100KHz Monolithic Bipolar DC/DC Converter," The European Power Electronics Association, pp.201-208, 1993	
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		Motorola, "Analog/Interface ICs Databook, MC34023," Vol. 1, pp. 3-247 – 3-262, 1995	
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	43	Maxim, "Dual-Output Power-Supply Controller for Notebook Computers, MAX786," May 1994	
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	52	Linear Technology, "LTC1435 High Efficiency Low Noise Synchronous Step-Down Switcher Regulator," pp. 4-212 – 4-225, 1996	

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				Filing Date	November 9, 2006
				First Named Inventor:	Balu Balakrishnan
				Art Unit	3992
				Examiner Name	~~Rubin, Margaret~~
Sheet	5	of	19	Attorney Docket Number	005510.P033R

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	53	Linear Technology, "LTC1435 High Efficiency Low Noise Synchronous Step-Down Switcher Regulator," pp. 4-212 – 4-225, 1996	
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	70	Unitrode Integrated Circuits, "UCC1810, UCC2810, UCC3810 Low Power BiCMOS Dual Current Mode PWM," pp. 6-145 – 6-151, December 1994	
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	72	P. HOROWITZ et al., "Laboratory Manual for The Art of Electronics," pp.17-1 – 17-3, August 6, 1981	
	73	National Semiconductor, "LM3001 Primary-Side PWM Driver ," pp. 3-140 – 3-159, 1995	

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	82	Power BiCMOS Current-Mode PWM," January 1995	
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	84	National Semiconductor, "Data Acquisition Databook," 1995	

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	85	Unitrode Integrated Circuits, "UC1828, 2828, 3828, 1840, 2840, 3840, 1841, 2841, 3841, 1848, 2848, 3848, 1851, 2851, 3851, 1854, 2854, 3854, 1874-1, -2, 2874-1, -2, Unitrode Current Mode PWM Controller IC," November 1994	
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				First Named Inventor:	Balu Balakrishnan
				Art Unit	3992
				Examiner Name	~~Rubin, Margaret~~
Sheet	12	of	19	Attorney Docket Number	005510.P033R

NON PATENT LITERATURE DOCUMENTS

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	130	NOTICE OF PRIOR ART PURSUANT TO 35 USC § 282, Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, August 31, 2006.	
	131	SUPPLEMENTAL NOTICE OF PRIOR ART PURSUANT TO 35 USC § 282, Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, November 3, 2006.	
	132	NOTICE OF PRIOR ART PURSUANT TO 35 USC § 282, Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, September 5, 2007.	
	133	PAUL HOROWITZ, expert for defendant Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENT NO. 6,107,851, 6,229,366, AND 6,249,876, pp. 1-27, submitted in relation to Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, November 30, 2005	
	134	EXHIBIT D, EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENT NO. 6,107,851, 6,229,366, AND 6,249,876, November 30, 2005, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent Anticipated or Rendered Obvious by Figure 1 of the '366 Patent, SMP 211 Datasheet ("SMP211") and PS07 Schematics ("PS07")*	
	135	EXHIBIT E, EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENT NO. 6,107,851, 6,229,366, AND 6,249,876, November 30, 2005, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent Anticipated or Rendered Obvious by Figure 1 of the '851 Patent, the Datasheet of Related Device PWR-SMP211, and PS07 Schematics*	
	136	EXHIBIT F, EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENT NO. 6,107,851, 6,229,366, AND 6,249,876, November 30, 2005, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'876 Patent Rendered Obvious by "Programmed Pulsewidth Modulated Waveforms for Electromagnetic Interference Mitigation in DC-DC Converters," by Wang and Sanders, Oct. 1993, IEEE Transactions on Power Electronics.*	
Examiner Signature			Date Considered		

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	137	ROBERT BLAUSCHILD, expert for Plaintiff Power Integrations, Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, REBUTTAL EXPERT REPORT OF ROBERT BLAUSCHILD, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, pp. 1-48, January 9, 2006	
	138	PAUL HOROWITZ, expert for defendant Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, pp. 1-26, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF	
	139	EXHIBIT B1 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF *'876 Patent, Claim 1, Anticipated by Martin.*	
	140	EXHIBIT B2 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'876 Patent, Claim 1, Rendered Obvious by Martin.*	
	141	EXHIBIT B3 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'876 Patent, Claim 1, Anticipated by Wang.*	
	142	EXHIBIT B4 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'876 Patent, Claim 1, Rendered Obvious by Wang.*	

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First Named Inventor:	Balu Balakrishnan
Art Unit	3992
Examiner Name	~~Rubin, Margaret~~
Attorney Docket Number	005510.P033R

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	143	EXHIBIT B5 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'876 Patent, Claim 1, Anticipated by Habetler.*	
	144	EXHIBIT B6 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'876 Patent, Claim 1, Rendered Obvious by Habetler.*	
	145	EXHIBIT C1 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 9, Anticipated by The TEA2262 Device.*	
	146	EXHIBIT C2 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 9, Rendered Obvious by The TEA2262 Device.*	
	147	EXHIBIT C3 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 14, Anticipated by The TEA2262 Device.*	
	148	EXHIBIT C4 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 14, Rendered Obvious by The TEA2262 Device.*	

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				Examiner Name	~~Rubin, Margaret~~
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	150	EXHIBIT C6 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *Figure 2 TEA2262 Device.*	
	151	EXHIBIT C7 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 14, Rendered Obvious by the TEA2262 Device in Light of Martin.*	
	152	EXHIBIT C8 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 14, Rendered Obvious by the TEA2262 Device in Light of Wang.*	
	153	EXHIBIT C9 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 9, Anticipated by the SMP260 Device.*	
	154	EXHIBIT C10 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 9, Rendered Obvious by the SMP260 Device.*	

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	155	EXHIBIT C11 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 14, Rendered Obvious by the SMP260 Device In Light of Martin.*	
	156	EXHIBIT C12 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 14, Rendered Obvious by the SMP260 Device In Light of Wang.*	
	157	EXHIBIT C13 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 9, Anticipated by the PWR-SMP3 Device.*	
	158	EXHIBIT C14 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 9, Rendered Obvious by the PWR-SMP3 Device.*	
	159	EXHIBIT C15 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 14, Rendered Obvious by the PWR-SMP3 Device in Light of Martin.*	
	160	EXHIBIT C16 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'366 Patent, Claim 14, Rendered Obvious by the PWR-SMP3 Device in Light of Wang.*	

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	163	EXHIBIT D3 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent, Claim 4, Anticipated by the TEA2262 Device.*	
	164	EXHIBIT D4 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent, Claim 4, Rendered Obvious by the TEA2262 Device.*	
	165	EXHIBIT D5 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *Block Diagram TEA2262 Device*	
	166	EXHIBIT D6 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *Figure 2 TEA2262 Device*	

Examiner Signature		Date Considered	

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1]Applicant's unique citation designation number (optional). [2]Applicant is to place a check mark here if English Translation is attached.

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /CEL/

EFS-Web Receipt date: 10/26/2007 90008326 - GAU: 3992

Substitute for Form 1449/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(use as many sheets as necessary)*				Application Number	90/008,326
				Filing Date	November 9, 2006
				First Named Inventor:	Balu Balakrishnan
				Art Unit	3992
				Examiner Name	~~Rubin, Margaret~~
Sheet	18	of	19	Attorney Docket Number	005510.P033R

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T[2]
	167	EXHIBIT D7 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent, Claim 1, Anticipated by Martin.*	
	168	EXHIBIT D8 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent, Claim 1, Obvious Combination of The TEA2262 And Martin*	
	169	EXHIBIT D9 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent, Claim 4, Obvious Combination of The TEA2262 And Martin*	
	170	EXHIBIT D10 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent, Claim 1, Rendered Obvious by Martin In Light of The SMP260 Device.*	
	171	EXHIBIT D11 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent, Claim 4, Rendered Obvious by Martin in Light of The SMP260 Device*	
	172	EXHIBIT D12 TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *'851 Patent, Claim 1, Rendered Obvious by Martin in Light of the SMP211 Device.*	

Examiner Signature		Date Considered	

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Based on Form PTO/SB/08B (08-03) as modified by BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP on 09/10/03.

EFS-Web Receipt date: 10/26/2007 90008326 - GAU: 3992

Substitute for Form 1449/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(use as many sheets as necessary)*				Application Number	90/008,326
				Filing Date	November 9, 2006
				First Named Inventor:	Balu Balakrishnan
				Art Unit	3992
				Examiner Name	~~Rubin, Margaret~~
Sheet	19	of	19	Attorney Docket Number	005510.P033R

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T[2]
	173	EXHIBIT E TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *Oscilloscope Plots of TEA2262 Test Circuit.*	
	174	EXHIBIT F TO SUPPLEMENTAL EXPERT REPORT OF PAUL HOROWITZ ON INVALIDITY OF US PATENTS NO. 6,107,851, 6,229,366 AND 6,249,876, December 29, 2006, *submitted in relation to* Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, *Untitled*	
	175	ROBERT BLAUSCHILD, expert for Plaintiff Power Integrations, SUPPLEMENTAL REBUTTAL EXPERT REPORT OF ROBERT BLAUSCHILD, Power Integrations, Inc., v. Fairchild Semiconductor International, Inc., and Fairchild Semiconductor Corporation, United States District Court for the District of Delaware, C.A. No. 04-1371-JJF, February 17, 2007.	

Examiner Signature	/Christopher E. Lee/	Date Considered	03/27/2008

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Search Notes	Application/Control No.	Applicant(s)/Patent Under Reexamination
	90008326	6249876
	Examiner	Art Unit
	Christopher E Lee	3992

SEARCHED			
Class	Subclass	Date	Examiner

SEARCH NOTES		
Search Notes	Date	Examiner
Reviewed file wrapper prosecution history	3/27/08	/CEL/
Application 09/192,959 references checked	3/27/08	/CEL/

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner

Index of Claims	Application/Control No.	Applicant(s)/Patent Under Reexamination
	90008326	6249876
	Examiner	Art Unit
	Christopher E Lee	3992

✓	Rejected	-	Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

☐ Claims renumbered in the same order as presented by applicant ☐ CPA ☐ T.D. ☐ R.1.47

CLAIM		DATE								
Final	Original	04/02/2008								
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BIB DATA SHEET

CONFIRMATION NO. 7651

SERIAL NUMBER	FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
90/008,326	11/09/2006 RULE	713	3992	10414-25

APPLICANTS

6249876, Residence Not Provided;
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Sanjeet K. Dutta(3RD PTY REQ), Menlo Park, CA;
Sanjeet K. Dutta, Menlo Park, CA;

**** CONTINUING DATA ***************************

This application is a REX of 09/192,959 11/16/1998 PAT 6,249,876

**** FOREIGN APPLICATIONS ***************************

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****

Foreign Priority claimed	35 USC 119(a-d) conditions met	Verified and Acknowledged	Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
☐ Yes ☑ No	☐ Yes ☑ No	/CHRISTOPHER E LEE/ Examiner's Signature	☐ Initials			32	6

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Los Angeles, CA 90025
UNITED STATES

TITLE

FREQUENCY JITTERING CONTROL FOR VARYING THE SWITCHING FREQUENCY OF A POWER SUPPLY

FILING FEE RECEIVED	FEES: Authority has been given in Paper
2520	No._____ to charge/credit DEPOSIT ACCOUNT No._____ for following:

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☐ 1.17 Fees (Processing Ext. of time)
☐ 1.18 Fees (Issue)
☐ Other _____
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Reexamination	**Application/Control No.** 90008326	**Applicant(s)/Patent Under Reexamination** 6249876
	Certificate Date	**Certificate Number**

Requester Correspondence Address: ☐ **Patent Owner** ☒ **Third Party**
Sanjeet K. Dutta ORRICK, HERRINGTON & SUTCLIFFE LLP 1000 Marsh Road Menlo Park, CA 94025

LITIGATION REVIEW ☒	/CEL/ (examiner initials)	03/27/2008 (date)
Case Name		Director Initials
Power Integrations v. Fairchild Semicond et al. 1:04cv1371		Eu Keasel for LMM

COPENDING OFFICE PROCEEDINGS	
TYPE OF PROCEEDING	**NUMBER**
1. Ex Parte Reexamination	90/008,324
2. Ex Parte Reexamination	90/008,327